

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re application of:

MARK A. BURNS

Serial No.: 10/026,053 (TI-33380)

Filed: December 21, 2001

For: PARALLEL INTEGRATED CIRCUIT TEST APPARAT US AND TEST METHOD

Group Art Unit: 2829

Examiner Jimmy Nguyen

Director of the United States
Patent and Trademark Office,
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby appeals to the Board of Appeals from the decision dated December 23, 2003 of the Primary Examiner finally rejecting claims 1 to 33, all of the rejected claims.

The item(s) checked below are appropriate:

1. ☒ An extension of time to respond to the final rejection
_____ was granted on _____
☒ is requested for one month(s).
2. ☒ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. ☒ Fee \$330.00:
_____ Not required (*Fee paid in prior appeal*)
☒ The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Account No. 20-0668. A duplicate copy of this sheet is enclosed.

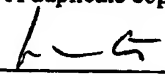
Signature [Rule 191(b)]

Post Office Address

(to which correspondence is to be sent)

04/20/2004 MAHHE01 00000035 200668 10026053

01 FC:1401 330.00 DA


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